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210314US-2524-57-2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

:

TOSHIAKI SHINOHARA

: EXAMINER: NGUYEN, D. P.

SERIAL NO: 09/895,319

:

FILED: JULY 2, 2001

: GROUP ART UNIT: 2814

FOR: SEMICONDUCTOR DEVICE AND:

METHOD OF MANUFACTURING

SAME

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

This communication is in response to the Office Action dated April 10, 2002.

IN THE CLAIMS

Please cancel Claims 9-11 without prejudice.

The following is a clean copy of Claims 1,3 and 5-8, with a marked-up copy

attached.

1. (Amended) A semiconductor device comprising:

a semiconductor element;

a lead frame having a first surface on which said semiconductor element is mounted, and a second surface opposite to said first surface;

a metal block on said second surface of said lead frame;

an insulation layer on said metal block opposite said lead frame; and

X

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